Course Number and Name

BEC 7L1 - DIGITAL CMOS VLSI LAB

Course Objectives

- To learn Hardware Descriptive Language(Verilog/VHDL)
- To learn the fundamental principles of VLSI circuit design in digital domain
- To familiarize implementation of logical modules on FPGAs

Prerequisites	Co-requisites
BEC3L2-Digital Electronics Lab	BEC702-Digital CMOS VLSI

Course Outcomes (COs)

CO1 Demonstrate a clear Understanding in hardware design language Verilog HDL

CO2 Model a Combinational circuit using hardware description language Verilog HDL and validate its functionality

CO3 Design and implement a sub system on a FPGA board..

CO4 Model a Sequential circuit using hardware description language Verilog HDL and validate its functionality

CO5 Demonstrate implementation of FPGA of ADC

CO6 To Understand the FPGA implementation of Traffic Light Controller

Student Outcomes (SOs) from Criterion 3 covered by this Course

COs/SOs	a	b	c	d	e	f	g	h	i	j	k
CO1	H				H	M		M		H	H
CO2	M	M	H		H	M	H		M		
CO3	M	H		H			L	M	M	M	M
CO4	M	H	H	M	H	M	H		M		
CO5		L		M					L		
CO6				M						L	